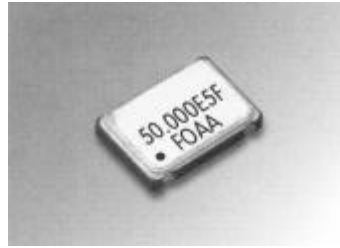


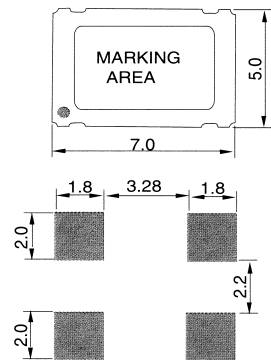
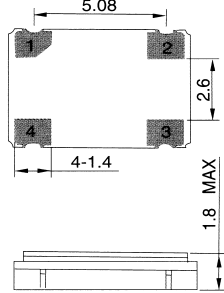
AQO 7050

Features/Merkmale

- Small Size 5x7mm/Kleines Gehäuse 5x7mm
- Low Cost to Performane/Gutes PreisLeistungsverhältnis
- Auto Pick and Place/zur automatische Bestückung
- Excellent for Lap Top and Hand Held PDA's
Sehr gut für kleine Produkte geeignet



Specifications/Spezifikationen			Remarks/Bemerkungen
		TTL/CMOS Logic	
Frequency range/Frequenz		1-80MHz	Please specify/Bitte angeben
Frequency Stability		± 25PPM - ±125PPM	Please specify/Bitte angeben
Operating Temp./Arbeitstemp.		-0°C - +70°C - -40°C - +85°C	Please specify/Bitte angeben
Storage Temp./Lagertemp.		-55°C - +125°C	
Input/ Eingang	Voltage/Spannung	2,7V - 5,5V	Please specify/Bitte angeben
	Current/Strom	10mA 1-4MHz	max
		30mA 4-32MHz	max
45mA 32-80MHz		max	
Output Symmetry/Ausgangs Symmetrie		40% - 60%, 45%-55%	
Output/ Ausgang	Rise and Fall Time/ Anstiegs und Abfallzeiten	5nS typisch, 10nS max	
		TTL	CMOS
	Logic"0"Level	+0,4V max	+0,5V(10%VDD)
	Logic"1"Level	+2,4V min	+4,5V(90%VDD)
Output Load		1-10TTL or/oder CMOS15-50pF	

Drawing/Zeichnung											
 <p style="text-align: center;">SOLDER PATTERN</p>	 <table border="1" style="margin-left: auto; margin-right: auto;"> <thead> <tr> <th>PIN</th> <th>FUNCTION</th> </tr> </thead> <tbody> <tr> <td>#1</td> <td>INH</td> </tr> <tr> <td>#2</td> <td>GROUND</td> </tr> <tr> <td>#3</td> <td>OUTPUT</td> </tr> <tr> <td>#4</td> <td>VDD</td> </tr> </tbody> </table>	PIN	FUNCTION	#1	INH	#2	GROUND	#3	OUTPUT	#4	VDD
PIN	FUNCTION										
#1	INH										
#2	GROUND										
#3	OUTPUT										
#4	VDD										

Dimensions/Abmessungen in mm

Remarks/Bemerkungen:

All specifications subject to change without notice. / Wir behalten uns vor Daten ohne Mitteilung zu ändern